

SMD Ceramic Clock-Oscillator Overall stability ± 100ppm Power Supply

Very low jitter 40 to 160MHz

FREQUENCY STABILITY

OVER:

OPERATING TEMP. RANGE: See note 1 **OVERALL STABILITY:** < ±100ppm \* INCLUDING:

**OVER OPERATING TEMPERATURE RANGE** 

ADJUSTMENT @ 25 ℃

LONG TERM AGING (10 YEARS)

STABILITY OVER SUPPLY VOLTAGE ±5% STABILITY OVER LOAD (MIN. TO MAX.)

**POWER SUPPLY** 

SUPPLY VOLTAGE: Vdd = 3.3V ±5% \*

**INPUT CURRENT:** < 30mA\*

**OUTPUT** 

**OUTPUT SIGNAL:** AC-MOS compatible \* SYMMETRY: 40 / 60% (min.) @ Vdd / 2\* **RISE & FALL TIME:** tr < 3ns tf < 3ns \* > Vdd - 0.5V LEVEL "0" & "1": < 0.4V

START-UP TIME: < 5ms FAN OUT (LOAD): 10 TTL / LS \* JITTER: < 1ps

**ENVIRONMENT** 

**OPERABLE TEMP. RANGE:** -55 to +125 ℃ STORAGE TEMP. RANGE: -65 to +125 ℃ VIBRATIONS: 10 to 2000Hz / 10g SHOCKS: 5000g, 0.3ms, 1/2 sine PACKAGE: Ceramic

**PACKAGE DIMENSIONS:** 14.1 x 9.3 x 2.4mm (see packaging info)

PROCESSING: Reflow soldering 260 ℃ / 10s max.

(see packaging info)

## **MISCELLANEOUS**

Customer's specification on request

Note 1: Operating Temperature Range

MCSOFV-A: 0 to +70 ℃ MCSOFV-B: -40 to +85 ℃ -55 to +125 ℃ MCSOFV-C:

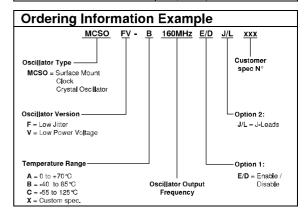
Option 1: Enable / Disable (on request) See application circuit on page 2 for details

Pin 3 (Fout):: Open Clock Н Clock High Z

Option 2: J / Leads (on request)

With tinned J / Leads pins Height: 3.8mm included J / Leads

Marking Example Micro Crystal Micro Crystal MCSOFV-B E/D Type Option 1 160.000 MHz Date Code 09.40 Frequency O (PIN 1) 0



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## STANDARD FREQUENCIES [MHz]

Date: June 2003 Revision No.: 4 Revision Date: 10-09 Page 1 / 2

In accordance with our policy of continuous development and improvement, we reserve the right to modify the design or the specifications of our products without prior notice.

Switzerland

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